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(54) **CIRCUIT AND METHOD FOR ADJUSTING THE CLOCK SKEW IN A COMMUNICATIONS SYSTEM**

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(57)   **ABSTRACT**

A circuit and method are disclosed for adjusting the clock skew in a synchronous system. The circuit and method include initially applying an offset voltage to a data input of a device in the system. Next, the clock skew between a device and a data source providing data thereto is adjusted to approximately 180 degrees, by selecting the clock skew resulting in an approximately maximum DC offset appearing at the output of the device. Thereafter, the clock skew is shifted from approximately 180 degrees to the desired clock skew amount.

43 Claims, 4 Drawing Sheets